

# Voltage-Controlled Skyrmion Memristor for Energy-Efficient Synapse Applications

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**Abstract**—A novel voltage-controlled skyrmion memristor (VCSK-Memristor) based on a multiferroic heterostructure is proposed and studied. Under electric-field-modulated magnetic anisotropy via remnant strain, continuously tunable resistance is obtained in a VCSK-Memristor due to the skyrmion size modulation in the ferromagnetic layer. Geometrical scaling studies on VCSK-Memristor are performed to provide the guidelines for the design and optimization of this newly proposed spintronic device. The results indicate that the VCSK-Memristor is advantageous for fabricating energy-efficient synapse arrays for hardware neural-network applications.

**Index Terms**—Skyrmion, spintronics, memristor, synapse, multiferroic heterostructure, magnetic tunnel junction.

## I. INTRODUCTION

DYNAMIC energy is considered as one of the most critical challenges for the continuing miniaturization of spintronic devices. One promising solution is to manipulate the magnetization in ferromagnetic (FM) materials using electric fields/voltages instead of magnetic fields or spin-torques produced by power-dissipating currents [1]. Electric field-modulated magnetization via remnant strain has been demonstrated in FM/ferroelectric (FE) multiferroic heterostructures [2]. The strain and the magnetization change are retained after removing the electric field and reversible under non-180° FE polarization switching [2]. Furthermore, continuously tunable remnant strain values in FE layer can be achieved by properly applying electric field from the polarized state [3]. Therefore, it is expected to continuously tune the magnetization via “non-volatile” remnant strain.

Magnetic skyrmions are topological spin textures with non-linear configuration in FM films [4], [5]. Current-driven skyrmion motion with assistance of electric fields has been studied [6], [7], where electric fields are used to control whether the skyrmion passes the control region or not but have little direct effect on the skyrmion itself, and the skyrmion

is considered as a rigid quasi-particle, having an achiral magnetization vector field. Although the small footprint and topological stability features of skyrmions are very much valued for bi-stable non-volatile memory (NVM) applications (*e.g.* skyrmion racetrack memory [8]), an unexplored topic in this field is to re-investigate the skyrmion’s feature for memristive devices, which takes advantages of skyrmion’s size modulation resulted continuous resistance change.

In this work, a novel multiferroic heterostructure is proposed with skyrmions induced in the FM layer, whose size is simply modulated by electric field control of magnetic anisotropy via remnant strain. The continuous change of magnetization is later read out through tunneling magneto-resistance (TMR). Therefore, a new class of spintronic devices is proposed as the voltage-controlled skyrmion memristor (VCSK-Memristor).

## II. DEVICE STRUCTURE AND SIMULATION METHODS

The basic concept of the VCSK-Memristor based on a multiferroic heterostructure consisting of FE/heavy metal (HM)/FM layers is depicted in Fig. 1a. A skyrmion can be initiated and stabilized at the center of the FM layer by injection of spin polarized current through a magnetic tunnel junction (MTJ) structure [9], [10]. Such MTJ devices with FE substrate have been investigated for strain-assisted magnetization switching for memory application [11], [12]. In our case, once a voltage is applied to the FE layer, a strain is generated and further transferred to the coupled FM layer, causing skyrmion size modulated. After applied voltage is removed, remnant strain is retained due to the preservation of non-180° FE polarization switching [2], [3] so as for the magnetization change of the FM layer. When using MTJ to read out the TMR, depending on the fraction of the reversed domain (*i.e.* skyrmion region), a continuous range of resistance values can be sensed.

A heterostructure with (011) PMN-Pt/[Co/Pt]<sub>n</sub> layers are simulated herein. [Co/Pt]<sub>n</sub> multilayer with perpendicular magnetic anisotropy (PMA) and *Dzyaloshinskii–Moriya* interaction (DMI) is used as the medium for inducing skyrmions [13], while electric field control of magnetization in (011) PMN-Pt/[Co/Pt]<sub>n</sub> has also been demonstrated [14], [15]. Magnetic [9] and elastic [16], [17] parameters of [Co/Pt]<sub>n</sub> used in simulation are listed in Table I. In addition, lead-free materials may also be exploited to equally work as the FE layer, *e.g.* BaTiO<sub>3</sub> [18].

Magnetization dynamics in the FM layer are simulated by solving the *Landau-Lifshitz-Gilbert* (LLG) equation under micro-magnetics scheme [19], in which the energy density

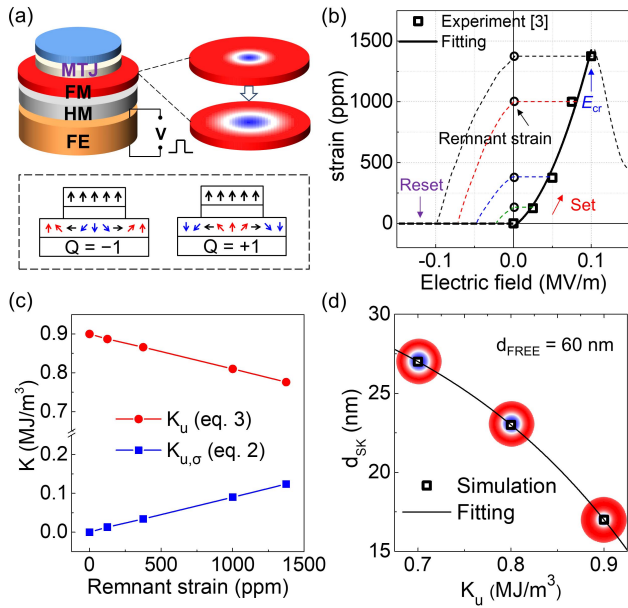
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**Fig. 1.** (a) Schematics of a voltage-controlled skyrmion memristor. A skyrmion can exist in two states with a skyrmion number  $Q = -1$  or  $+1$ . (b) Strain response of (011) PMN-PT under electric field. (c) Perpendicular magnetic anisotropy constant ( $K_u$ ) and stress-induced anisotropy constant ( $K_{u,\sigma}$ ) under different remnant strain. (d) Skyrmion diameter ( $d_{SK}$ ) and snapshots of FM layer under different  $K_u$ .

**TABLE I**  
SIMULATION PARAMETERS OF [CO/PT]<sub>N</sub>

	Parameters	Values
Magnetic [9]	Saturation magnetization, $M_s$	580 kA/m
	Initial PMA constant, $K_{u0}$	0.9 MJ/m <sup>3</sup>
	DMI constant, $D$	3.0 mJ/m <sup>2</sup>
	Exchange constant, $A_{ex}$	10 pJ/m
	Gilbert damping constant, $\alpha$	0.3
Elastic [16], [17]	Magnetostriction constant, $\lambda$	290 ppm
	Young's Modulus, $Y$	180 GPa
	Poisson's ratio, $\nu$	0.31
Geometric [9]	Simulation mesh size (one site in thickness direction)	$1 \times 1 \times 1$ nm <sup>3</sup>

consists of PMA ( $E_k$ ), exchange, DMI, and demagnetization effects. The relationship between the strain ( $\varepsilon$ ) in FE layer and applied electric field ( $E$ ) is extracted from experiment results of (011) PMN-PT [3], as shown in Fig. 1b. From a polarized state (referring to zero strain),  $E$  below the critical value ( $E_{cr}$ ,  $\sim 0.1$  MV/m, where a strain jump peak occurs) can cause the non-180° FE polarization switching and set a remnant strain value ( $\varepsilon_r$ ). Different  $\varepsilon_r$  depends on the proportion of the switched non-180° FE polarization controlled by amplitude of  $E$ , and quasi-continuous  $\varepsilon_r$  (from 0 to  $\sim 1375$  ppm) is expected. A reversed electric field can switch FE layer to the initial polarized state, denoted as the Reset operation.

The associated stress ( $\sigma$ ) produced by the remnant strain that acts on FM layer is described by Hooke's Law [20]

$$\sigma = \frac{Y}{1 - \nu^2} \varepsilon_r, \quad (1)$$

with stress-induced anisotropy  $K_{u,\sigma}$  evaluated by [16]

$$K_{u,\sigma} = -\frac{3}{2} \lambda \sigma. \quad (2)$$

The overall PMA constant  $K_u$ , which relates to  $E_k$  in micro-magnetic simulations, is expressed as

$$K_u = K_{u0} + K_{u,\sigma}. \quad (3)$$

Figure 1c shows resulted  $K_u$  variation under different  $\varepsilon_r$  according to eqs. (1)–(3), to justify that the strain induced PMA tuning is achievable in this multiferroic heterostructure. A larger  $K_u$  would shrink the size of a skyrmion, since it favors a stronger PMA to dominate the total energy terms, which is reflected on the skyrmion diameter ( $d_{SK}$ , defined as the spacing of intercepts of  $m_z = 0$ ) vs.  $K_u$  curves as shown in Fig. 1d. Thus, when  $E$  varies from 0 to 0.1 MV/m (accordingly,  $K_u$  changes from 0.9 to 0.78 MJ/m<sup>3</sup>), the resulted  $d_{SK}$  has a range from 17 to 24 nm in an FM layer with diameter ( $d_{FREE}$ ) of 60 nm. Therefore, it is proved that  $d_{SK}$  can be modulated by the applied voltage in the FE layer.

As a proof-of-concept, the strain-mediated effect model has been used for capturing the electric field modulation of magnetization. In practical applications, with various effects involved, such as interface [14] and charge effects [21], a same  $E$  value to induce a larger variation on  $K_u$  is expected. On the other hand, the strain may also change other magnetic properties such as DMI constant ( $D$ ), saturation magnetization ( $M_s$ ). However, previous studies show that, in Co/Pt-based material systems,  $D$  variation is just 0.2% by up to 0.1% strain [22], and  $M_s$  is almost unchanged under strain [15]; whereas magnetic anisotropy could change significantly, even for a large rotation between out-of-plane and in-plane directions [14]. Thus, the strain-induced variation of  $D$  and  $M_s$  is neglected in this work.

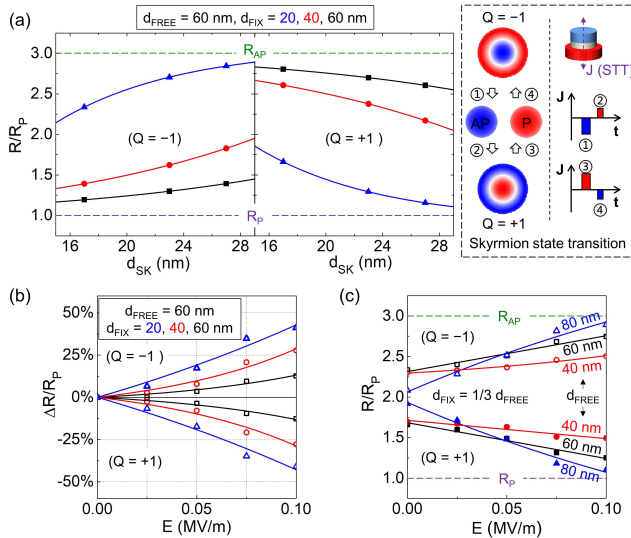
### III. VCSK MEMRISTOR OPERATIONS AND PERFORMANCE

The tunneling resistance ( $R$ ) of an MTJ can be expressed as

$$R = R_P + \frac{1}{2}(R_{AP} - R_P)(1 - \cos \theta) \quad (4)$$

where P and AP refer to parallel (P) and antiparallel (AP) states, respectively, and  $\theta$  is the magnetization angle between fixed and free layers [23]. In this work, a skyrmion can exist in the free layer in two states with a skyrmion number [24], [25]  $Q = -1$  and  $+1$  to identify the core of skyrmion pointing downward and upward, respectively (Fig. 1a). The normalized perpendicular component of magnetization ( $m_z$ ) in the free layer beneath the fixed layer is calculated from simulation data as  $\cos \theta$  when magnetization in the fixed layer lies upward. The diameter ( $d_{FIX}$ ) of fixed layer can be varied to obtain different  $m_z$  and therefore different ranges for resistance tuning.

The normalized resistance  $R/R_P$  versus  $d_{SK}$  is then calculated and shown in Fig. 2a, where a TMR ( $= (R_{AP} - R_P)/R_P$ ) of 200% [26] is assumed. The electric field induced resistance modulation ( $\Delta R$ ) is shown in Fig. 2b. Clearly, the resistance can be effectively controlled by the applied voltage in a large range, enabling this device to be functioned as a memristor [27]. To well sense the continuous change of the skyrmion size and the dynamic resistance range, an MTJ with higher TMR is preferred, along with stronger electric field controlled-magnetization effect to induce larger skyrmion size variation. On the other hand, two skyrmion states are

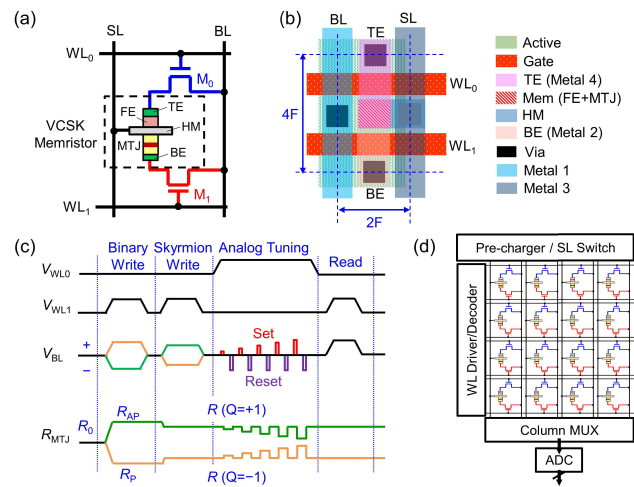


**Fig. 2.** (a) Normalized resistance  $R/R_p$  as a function of  $d_{SK}$  under different  $d_{FIX}$  at  $d_{FREE} = 60$  nm and illustration of the transition of two skyrmion states via STT. (b) Electric field ( $E$ ) induced resistance modulation ( $\Delta R$ ). (c)  $R/R_p$  as a function of  $E$  in VCSK-Memristor with different  $d_{FREE}$ . TMR = 200% is assumed during the calculation.

transferrable to each other by applying programming current pulses through the MTJ via spin transfer torque (STT) [10] (Fig. 2a). Hence, two separate levels of resistance values (*i.e.* both the cases of  $Q = -1$  and  $+1$ ) can be implemented within one device. Note that the application of current-induced STT would produce transient *Joule* heating, while an experiment study demonstrated that skyrmion state is preferred and stable under thermal activation in skyrmion-based material system [28].

VCSK-Memristor is a nano-pillar structure with its planar area equal to the FM free layer. The devices with different sizes from 80 to 40 nm are investigated to evaluate their scaling potentials for high-density array applications. Comparable resistance tuning capabilities are found as shown in Fig. 2c, which ensures the scalability of the proposed device. However, there exists a design trade-off between the device's size and dynamic resistance range, which is due to the enhanced boundary repulsion effect on skyrmion expansion (*i.e.* reduced tunability of skyrmion size) at reduced size.

The non-volatility (*i.e.* retention) is determined by the remnant strain, which as shown in [3], can be stable up to days and has no significant degradation under enduring voltage cycles. However, the application of periodic strain cycling may generate the pinholes resulting in the degradation of TMR, to avoid which, future applications need to determine the optimal range of strain induced. The program (write) speed of VCSK-memristor is determined by FE polarization switching in FE layer (for fast switching, can be  $<10$  ns [29]), while the read time is similar to existing MRAMs ( $<20$  ns [30]). Due to the non-volatile property, the power consumption is dominated by dynamic voltage-induced FE polarization switching process. For a full-swing switching, the power can be estimated as  $P_r S V / 2$  [29], where  $P_r$  ( $30 \mu C / cm^2$  [3]) is the remnant polarization,  $S$  the area, and  $V$  the voltage amplitude. Providing our device with 60 nm diameter and 100 nm FE thickness, a voltage of  $V = 0.1 MV / m \times 100 nm = 10$  mV corresponds to an ultra-low energy consumption of  $\sim 5$  aJ



**Fig. 3.** (a) Illustration of a 2T1M VCSK-based synapse cell. (b) Layout design with a cell size of  $8F^2$ . (c) Schematic of operations and sequence diagram. (d) Synaptic core array and periphery circuit designs.

per operation. This energy consumption is drastically lower compared with that of ReRAM ( $\sim 0.1$  pJ) and PCM ( $\sim 10$  pJ) based memristors [31].

#### IV. VCSK-BASED SYNAPSE DESIGN

Memristor technologies have been exploited as synapse-like functions for self-adaptive networks [32]. The 3-terminal VCSK-Memristor can be used for synapse applications with its read-out resistance as the synapse weight. Figure 3a and b show the VCSK-based synapse cell and layout design with the size of  $8F^2$  ( $F$ : feature size). A standard synapse cell consists of two transistors ( $M_0$ ,  $M_1$ ) and a VCSK-Memristor, connected by Word lines ( $WL_0$ ,  $WL_1$ ), Bit line (BL), and Source line (SL) in back-end-of-line (BEOL) layers. During each programming cycle,  $M_0$  is turned on to apply the voltage pulse on the FE layer, while  $M_1$  is selected during each read cycle. The overall operations are summarized in Fig. 3c. With  $WL_1$  voltage ( $V_{WL1}$ ) on and  $WL_0$  voltage ( $V_{WL0}$ ) off, two skyrmion states could be selected by programming the BL voltage ( $V_{BL}$ ). With  $V_{WL0}$  on and  $V_{WL1}$  off, the desired MTJ resistance can be set by applying  $V_{BL}$  with different amplitude after Reset operation. A small  $V_{BL}$  is used to perform the Read operation. Compared with existing proposals of skyrmion-based synapses [33], [34], which use current-induced spin orbit torque (SOT) to drive skyrmion motion and require a large footprint and power consumption (due to self-heating effect), VCSK-based synapse cell is advantageous for fabricating high-density and energy-efficient array, as illustrated in Fig. 3d.

#### V. CONCLUSION

A VCSK-Memristor of FE/HM/MTJ structure is proposed. Physics-based simulations suggest that the readout resistance values can be efficiently modulated by applying voltage as well as the assistance of STT. Geometry scaling studies further reveal that the memristive behavior is available for this new device with different size, guaranteeing its robustness under process variability. The VCSK-Memristor cells have a promising potential to fabricate energy-efficient synapse arrays for constructing hardware neuromorphic networks.

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