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Abstract --- Nanoelectromechanical (NEM) switches could surmount the Boltzmann Tyranny in the current chargecarrier systems. However, thus far, practical implementations of the NEM systems have been hindered by the complicated fabrication processes of forming the extremely small air gap. Here, we realize a very simple NEM switch by exploiting a switchable nanocrack controlled by an electric field in a metallic alloy-ferroelectric heterostructure. The crack is formed in a controllable manner in terms of its initiation, location, and orientation through a bridge-like structure. The open and closed states of the crack are programmed under a cyclic electric field. In addition, an abrupt switching behavior with a nonvolatile high ON/OFF current ratio (>10<sup>7</sup>) is measured owing to the near-zero OFF-state leakage current across the crack. This simple nanocrack switch presents a novel opportunity in the NEM systems, which can be used as a new nonvolatile random-access memory and logic.

# Index Terms—Nanoelectromechanical switch, controlled nanocrack, electric field control, nonvolatile memory.

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## I. INTRODUCTION

▼RACKS are generally considered as defects in electronic devices. However, many intriguing crack-based structures and devices have been demonstrated, such as ultrasensitive mechanical sensors [1]–[3], high-resolution patterning [4]–[6], and electronic nanogaps [7]-[8]. Most of these utilized the "micro-notch" structures and confined surface stress to form the cracks in thin brittle materials [2]-[10]. Recently, another new type of cracks in a metallic alloy-ferroelectric heterostructure, which can open or close reversibly under electric field, has been reported [11]-[12]. The reversible opening and closing of the cracks can induce a huge resistance change in the alloy film, illustrating the potential for the future memory or logic. Nevertheless, it is hindered by the random distribution of cracks throughout the structure, which is the main obstacle to actual application. Critically, the precise control of nanocracking is the key factor for a variety of applications [4]-[7], [10].

The mechanical switching process of the crack with nanoscale width resembles the nanoelectromechanical (NEM) switch, which operates by nanometer-scale motion to make and break physical contact between electrodes and hence do not rely on energy-barrier modulation as the switching mechanism [13]–[17]. Here, we demonstrate the direct control of location, orientation and electrical switching of nanocrack, by using a "bridge-like" structure in the alloy film-ferroelectric heterostructure. Based on such structure, a novel NEM switch with easy fabrication process, high on/off ratio, and low power consumption is also proposed.

# II. CRACK-BASED NEM SWITCH CONCEPT

Our device consists of a conductive alloy thin film grown on top of a ferroelectric substrate. The conductive thin film is patterned to outline a bridge-like structure as illustrated in Fig. 1a. Next, the crack-forming process is conducted through by applying a cyclic electric field along the thickness direction of the substrate. Then, the crack initiates at the edge of the active area and is oriented in the width direction (the crack location is discussed in detail later). More importantly, the crack state (open or closed) can also be controlled by the electric field. A negative electric field results in the opening of the crack (Fig. 1b), whereas a positive one can lead to the closed state (Fig. 1c). Accordingly, the opening and closing of the crack can break and connect the current path in the alloy film, constructing a NEM switch.

We demonstrate such a crack-based NEM switch using a  $Mn_{50}Pt_{50}$  alloy film and (001)-oriented PMN-PT single-crystal substrate.  $Mn_{50}Pt_{50}$  was chosen because of its intermediate

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Fig. 1. (a) Schematic of the device, which consists of a bridge-like patterned alloy thin film and ferroelectric substrate. (b) Open state of the crack under a negative electric field. The current path in the alloy thin film is broken by the open crack. (c) Closed state under a positive electric field. The corresponding current path is connected. The cross-section SEM images of the (e) open state and (f) closed state of the crack at the same location (d) are based on the PMN-PT/MnPt heterostructure controlled by an electric field. The dashed squares in (e) and (f) represent the FIB etched window.



Fig. 2. (a) Schematic of the heterostructure without patterning and twoprobe resistance measurement setup. The positive direction of  $V_{\rm C}$  is defined from the bottom to the top electrode. (b) and (c) show the partial optical microscopy images of the cracks in the macroscopic thin film after scanning  $V_{\rm C}$  from (b) -100 V and (c) +100 V to 0 V, where the points A, B and C represent different probe locations for measurement. (d) and (e) show the current  $l_{\rm a}$  as a function of  $V_{\rm C}$  collected from two probes located at (d) points A and B and (e) points A and C, respectively. Inset shows electrical waveform for both generating and manipulating the cracks. Notice that (d) and (e) show different on/off ratios.

mechanical properties, which are very close to the empirical boundary between ductile and brittle materials [12]. To clearly observe the configuration of the NEM switch, cross-section scanning electron microscope (SEM) images of the crack in different states (Fig. 1e and f) were acquired using a focused ion beam (FIB). It is clear that the crack extends from PMN-PT into the MnPt film, which forms a metal-metal nano-gap in the film. The actuation of conventional NEM switches includes a variety of methods, for example, electrostatic, thermal, piezoelectric and resonant switching [13]. However, the top-down approach for fabrication requires complicated processing steps involving electronic beam lithography, nanomachining and coating technologies. By contrast, here, the manipulation of a controlled crack by an electric field enables the development of a new simple approach for the implementation of NEM switches.

# **III. VOLTAGE CONTROL OF CRACKS IN THIN FILMS**

Firstly, we investigated the properties of cracks generated in a macroscopic MnPt thin film. As shown in Fig. 2a, a 40-nm MnPt film was deposited on a PMN-PT substrate (5 mm  $\times$  5 mm  $\times$  0.5 mm) at room temperature by magnetron

sputtering. Then a triangular voltage  $V_{\rm C}$  (inset in Fig. 2d) up to 300 V with a period of t = 4 min was applied between the top MnPt film and the bottom Ag electrode to induce cracks. After the forming process, the crack states (open or closed) were manipulated under  $V_{\rm C}$  up to 100 V (larger than the switching field) with t = 80 s. When V<sub>C</sub> varied from -100 to 0 V, the cracks were distinctly visible by optical microscopy (Fig. 2b), indicating the open state of the cracks. However, virtually all the cracks disappeared after scanning  $V_{\rm C}$  from +100 to 0 V (Fig. 2c). The optical images show that multiple cracks are generated in the MnPt film after a single application of the electric field, and that the cracks are distributed randomly. In addition, the generated cracks intersect with boundaries or with each other (indicated by the dashed circle in Fig. 2b), complicating the morphology of the cracks.

Then the electrical performance of the cracks was measured through a two-probe resistance measurement (Fig. 2a). The current  $I_a$  was measured when a constant voltage of 0.1 V was applied between the two probes. Fig. 2d shows one of the results, which was collected from the two probes located at A and B as labeled in Fig. 2b. A positive  $V_{\rm C}$  higher than 25 V induces a high  $I_a$  of approximately 0.1 mA, while  $I_a$ is reduced to 10 nA under negative  $V_{\rm C}$  lower than -60 V, resulting in a decline in  $I_a$  of four orders of magnitude. When we measured the result between the probes at points A and C, a similar square-like loop was observed; however, the variation in  $I_a$  was several orders of magnitude smaller. On sweeping  $V_{\rm C}$  from -100 to 100 V,  $I_{\rm a}$  varies from just 0.13 to 0.21 mA (Fig. 2e). The different  $I_a$  variations between the two results are associated with the random distribution of the cracks in the film. Here, test point B is surrounded by cracks. Thus, when those cracks are open, very little current can flow between A and B, and thereby a low value of  $I_a$  is observed. However, A and C are not separated completely by the cracks, and electrical percolation still exists between these two points, even when the cracks are open. This results in only a small decrease in  $I_a$ .

From these results, it can be found that the electrical performance of the cracks in the macroscopic film strongly depends on the test point positions, which is attributed to the random distribution of the cracks. Therefore, further precise control of crack is urgently required for actual applications.

## **IV. PRECISE CONTROL OF CRACK**

Next, we used photolithography followed by etching to pattern the bridge-like structure. Fig. 3a shows the schematic of the patterned device and the measurement setup.

The method for generating a crack in the bridge-like device is similar to that in the film. At the end of each  $V_{\rm C}$  cycle (inset in Fig. 2d), the maximum of  $V_{\rm C}$  ( $V_{\rm C(max)}$ ) gradually increased in 10 V intervals until the crack was induced. In our experiment, when  $V_{\rm C(max)}$  reached 130 V, a crack was generated at the edge of the active area and propagated across the width of the bridge structure. Fig. 3b shows the nonvolatile open state of the generated nanocrack (~50-nm width) when  $V_{\rm C}$  was varied from -100 to 0 V, whereas the closed state is shown in Fig. 3c. Accordingly,  $I_{\rm a}$  (device 1) decreased sharply from ~150  $\mu$ A to near zero upon applying the negative  $V_{\rm C}$ with a magnitude of -65 V, and  $I_{\rm a}$  reached ~0.4 pA when  $V_{\rm C}$  recovered back to 0 V, as indicated by arrows 1 and 2 in Fig. 3d. Symmetrically, voltages of approximately +65 V switched  $I_{\rm a}$  from 10 pA to the saturated value, and  $I_{\rm a}$  was



Fig. 3. (a) Schematic of the device with the bridge-like structure and the corresponding resistance measurement setup. (b) and (c) show the top-view SEM images of the active area with the (b) open and (c) closed state of the crack. (d) The current  $I_a$  as a function of  $V_C$  collected from different devices. The insets show the SEM images of other two devices. The voltage step and time interval of  $V_C$  were fixed at 1 V and 1 s, respectively. (e) Repeatability test for the switching of crack via voltage pulses (the inset shows the pulse details). The amplitude and duration of the pulse were 100 V and 2 s, respectively.

unchanged as  $V_{\rm C}$  was swept from +100 to 0 V, as represented by arrows 3 and 4 in Fig. 3d. The *I-V* characteristics of the other two devices (same size as the device 1) are also demonstrated in Fig. 3d, showing similar square loops.

A repeatability test was also performed, as shown in Fig. 3e. Alternating voltages of  $\pm 100$  V with a time interval of 2 s were applied, and  $I_a$  was measured during voltage application. The positive voltage results in high  $I_a$  values (represented by red dots), while the negative voltage induces low  $I_a$  values (indicated by blue dots). Neither the high nor the low values of  $I_a$  changed greatly during the several hundred tests. However, the crack did not close completely with increasing cycling times, resulting in an increase in ON contact resistance and failure of the device. We attribute this to the contact contamination and the uneven contact surface of the crack. To further improve the reliability, a contact material with suitable mechanical properties and high robustness to the surface contamination, such as oxidation, hydrocarbon and tribopolymer formation, can be employed in the device [18]. Moreover, a ferroelectric layer with a regular domain structure can be used to form the smooth crack surface for reliability optimization.

With such a patterned structure, the crack location can be confined within certain regions, despite the random distribution of cracks in the macroscopic thin film. The crack orientation was also along the specific direction.

Now, we briefly discuss the mechanism of switching of the crack to understand the reason for realizing such precise control. The applied electric fields switch the ferroelectric domain and produce a large strain at the domain boundary. The crack is induced to relax the severe increase in elastic energy, which also introduces additional surface energy. Therefore, the crack forms easily in zones in which the ferroelectric domain switching occurs more easily, or in other words, those with higher electric field distributions. After the forming process, the switching of the crack under electric field is governed mainly by the competition between the electromechanical-strain-related elastic energy and the surface energy [12]. In addition, owing to the nonvolatile switching of



Fig. 4. (a) Top-view  $E_Z$  distribution in the bridge-like structure using COMSOL Multiphysics simulation. (b) The enlarged view for the active area. The cracks induced by electric field in devices with different active area sizes (c) 6  $\mu$ m × 30  $\mu$ m, (d) 5  $\mu$ m × 30  $\mu$ m, (e) 7  $\mu$ m × 40  $\mu$ m, (f) 3.5  $\mu$ m × 30  $\mu$ m, (g) 3  $\mu$ m × 30  $\mu$ m, (h) 0.3  $\mu$ m × 6.5  $\mu$ m. The dashed lines in (e) and (g) are used to indicate the crack.

the 109° domain in the PMN-PT, the switching of the crack also exhibits the nonvolatility [19].

Hence, we analyzed the electric-field distribution in our bridge-like device. Fig. 4a shows the z-component distribution of the perpendicularly applied electric field  $(E_Z)$  in the device; this is the main factor determining the domain switching. It is clear that the distribution of  $E_Z$  is not uniform in the patterned device. In fact, the highest values of  $E_Z$  are distributed mainly at the corners, as indicated by the red zones in Fig. 4a. In particular, in the active area, the highest values of  $E_{Z}$  are distributed at the edges, as indicated by the dashed circle in Fig. 4b, and the distributions of  $E_Z$  are axisymmetric relative to the x-axis. Critically the crack generated in our fabricated device shown in Fig. 3b and 3c is located exactly in this zone and is oriented along the width of the bridge structure. Moreover, the cracks generated in other devices of different sizes (Fig. 4c-h) are also located in the same or similar zone(s). The excellent agreement between the theoretical simulation and the experimental observation confirms the feasibility of our method.

The resistance change in our device is achieved by the voltage (corresponding to a small electric field of  $\sim 1.3$  kV/cm) applied to the highly insulating ferroelectric substrate, and the Joule heating is negligible. As a result, the switching energy can be ultralow once such a device is scaled down to the nanoscale. The writing energy comprises two parts: one is for switching the polarization and the other for the additional surface energy. The ferroelectric polarization switching energy can be estimated using  $P_r SV/2$  ( $P_r$  is the remnant polarization, S is the cell area and V is the switching voltage) [20] and the surface energy can be calculated as  $\gamma A (\gamma)$  is the surface energy density and A is the surface area) [12]. For a PMN-PT film with a thickness of 2  $\mu$ m, the switching voltage can be reduced dramatically to 1.3 kV/cm  $\times$  2  $\mu$ m = 0.26 V. Considering a scaled-down active area with a size of 100 nm (length)  $\times$ 50 nm (width)  $\times$  25 nm (thickness of the MnPt film plus crack depth in the PMN-PT film), the calculated total writing energy per bit is 2.695 fJ.

#### V. CONCLUSION

The precise control of the nanocrack including the location, orientation, and electrical switching has been demonstrated in the PMN-PT/MnPt heterostructure. Based on such the switch-able nanocrack, the NEM switch with a simple fabrication process is also proposed. The crack-based NEM switch will not only provide new opportunities for the nonvolatile memory and logic, but also revolutionize the development of novel multifunctional sensors and IoT products.

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